

Features:

- Antenna input with SMA connector
- On board low noise 3.3 V voltage regulator
- USB A connector
- 3 virtual USB ports for all 3 serial ports of the GNSS receiver
- Time pulse LED, Power LED
- BY_Connect Evaluation Software
- Extensive visualization and evaluation features
- Backup supercapacitor
- Dimensions 85x30x15 mm
- Fully assembled and ready to use

Reference: ELT0602



MODULE FEATURES

Based on the new generation GNSS SoC - Alice, designed on the basis of ISO26262 ASIL B functional safety

Supports on-chip RTK positioning calculation on all systems and multiple frequencies

Supports GPS L1C/A/L1C/L2P(Y)/L2C/L5, BDS B1I/B2I/B3I/B1C/B2a/B2b, GLONASS G1/G2/G3, Galileo E1/E5a/E5b/E6, QZSS L1C/A/L1C/L2C/L5, NavIC L5 and SBAS L1C/A

All-system multi-frequency RTK engine and advanced RTK technology

Independent tracking of different frequencies and 60dB narrowband anti-jamming technology

16.0 x 12.0 x 1.8 mm LGA package

PERFORMANCE

Frequency	GPS L1C/A/L1C*/L2/L5 BDS B1I/B2I/B3I/B1C*/B2a/B2b* GLONASS G1/G2 Galileo E1/E5a/E5b/E6* QZSS L1/L2/L5/L6(CLAS*) NavIC L5 SBAS* L1C/A
Single point positioning (RMS):	Horizontal: 1.5 m Vertical: 2.5 m Data Update Rate 10 Hz Positioning
RTK (RMS):	Horizontal: 1 cm + 1 ppm Initialization Reliability > 99.9% Vertical: 1.5 cm + 1 ppm Data Update Rate 10 Hz Positioning
Sensitivity:	Acquisition: -144 dBm Re-acquisition: -154 dBm Tracking: -154 dBm
Accuracy:	Carrier phase: <1mm (RMS) Pseudorange: <0.1m (RMS) Tracking: -154 dBm Time Accuracy (RMS): <20 ns Speed measurement: 0.03 m/s (RMS)
Anti-jamming protection:	Jamming to signal ratio: 65dBc Multitone jamming: 6 or more pulses Narrowband jamming: 10% of the signal bandwidth Fast sweep jamming protection

OTHER FEATURES

Physical Characteristics:	Packaging: 79 pin LGA Dimension: 16.0 × 12.0 × 1.8 mm Weight: 1.88 ± 0.03 g
Environmental Specifications:	Working temperature: -40 °C ~ +105 °C Storage temperature: -55 °C ~ +150 °C Humidity: 95% No condensation Vibration: MIL-STD-810F Shock: MIL-STD-810F
Communication Interfaces:	3 × UART (LVTTTL)